

O I P E
FEB 25 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Hideo SHIMIZU, et al.

SERIAL NO: 10/720,064

GAU:

FILED: November 25, 2003

EXAMINER:

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, SEMICONDUCTOR INTEGRATED CIRCUIT MODULE AND INFORMATION APPARATUS

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

- Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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DOCKET NO.: 245785US2SRD/mtm

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

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FOR: SEMICONDUCTOR INTEGRATED CIRCUIT, SEMICONDUCTOR
INTEGRATED CIRCUIT MODULE AND INFORMATION APPARATUS**STATEMENT OF RELEVANCY****Reference AA (US 4,275,405) on Form PTO-1449:**

This discloses a semiconductor timing device with radioactive material that is not suitable for LSI and not used in the present invention.

Reference AB (US 5,760,644) on Form PTO-1449:

This discloses an integrated circuit timer that realized by SONOS or SNOS structure devices. But these structures are not connected directly by to the present invention.

Reference AO (JP 10-189780) on Form PTO-1449:

This discloses an integrated circuit timer realized by MONOS or MNOS structure devices. But these structure devices are not connected directly to the present invention.

Reference AP (JP 10-135882) on Form PTO-1449:

This reference is discussed in the specifications.

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SHEET 1 OF 1
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| Form PTO 1449 (Modified) | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | ATTY DOCKET NO. 245785US2SRD | SHEET 1 OF 1 SHEET 1 OF 1 | | SERIAL NO. 10/720,064 |
|--|----|--|------------|---------------------------------|------------------------------|--|----------------------------|
| LIST OF REFERENCES CITED BY APPLICANT | | APPLICANT Hideo SHIMIZU, et al. | | | | | |
| | | FILING DATE November 25, 2003 | | | GROUP | | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER INITIAL | | DOCUMENT NUMBER | DATE | NAME | CLASS | SUB CLASS | FILING DATE IF APPROPRIATE |
| AA | | 4,275,405 | 06/23/1981 | J.M. SHANNON | | | |
| AB | | 5,760,644 | 06/02/1998 | L.T. LANCASTER, et al. | | | |
| AC | | | | | | | |
| AD | | | | | | | |
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| AN | | | | | | | |
| FOREIGN PATENT DOCUMENTS | | | | | | | |
| | | DOCUMENT NUMBER | DATE | COUNTRY | TRANSLATION | | |
| AO | | 10-189780 | 07/21/1998 | JAPAN | YES | | X |
| AP | | 10-135882 | 05/22/1998 | JAPAN | YES | | X |
| AQ | | | | | | | |
| AR | | | | | | | |
| AS | | | | | | | |
| AT | | | | | | | |
| AU | | | | | | | |
| AV | | | | | | | |
| OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.) | | | | | | | |
| | AW | | | | | | |
| | AX | | | | | | |
| | AY | | | | | | |
| | AZ | | | | | <input type="checkbox"/> Additional References sheet(s) attached | |
| Examiner | | | | Date Considered | | | |
| *Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. | | | | | | | |